Listing of Claims:

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- Claim 1: (previously presented) A liquid crystal display, each pixel area comprising:
- a pixel electrode enclosed by a first gate line, a second gate line, a first data line, and a second data line;
- 5 a switching element positioned on the first gate line;
 - a first shielding layer having an edge between the first gate line and the pixel electrode; and
 - a second shielding layer having an edge between the second data line and the pixel electrode, wherein the width of the first shielding layer is larger than the width of the second shielding layer and the switching element is adjacent to the first shielding layer.
 - Claim 2: (previously presented) The liquid crystal display as claimed in claim 1, wherein the first shielding layer overlaps the periphery of the pixel electrode.
 - Claim 3: (previously presented) The liquid crystal display as claimed in claim 1, wherein the first shielding layer directly connects to the first gate line.
- Claim 4: (previously presented) The liquid crystal display as claimed in claim 1, wherein the second shielding layer directly connects to the first gate line.
 - Claim 5: (previously presented) The liquid crystal display as claimed in claim 1, wherein a spacing between the first data line and the pixel electrode is a liquid crystal reverse region.
 - Claim 6: (previously presented) The liquid crystal display as claimed in claim 1, wherein a spacing between the second data line and the pixel electrode is a liquid crystal non-reverse region.

Claim 7: (previously presented) The liquid crystal display as claimed in claim 1, further

comprising a repair line situated across the first shielding layer and the second shielding

layer.

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Claims 8-28: (canceled).

Claim 29: (Previously presented) The liquid crystal display as claimed in claim 1,

wherein the second shielding layer overlaps the periphery of the pixel electrode.

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Claim 30: (withdrawn) The liquid crystal display as claimed in claim 5, wherein a spacing

between the first data line and the periphery of the fist shielding layer is smaller than a

spacing between the second data line and the periphery of the second shielding layer.

Claim 31: (previously presented): The liquid crystal display as claimed in claim 1,

wherein the first shielding layer partially overlaps an extension portion of the first data

line.

Claim 32: (previously presented) The liquid crystal display as claimed in claim 7, wherein

the repair line is used for providing an operative path when the first gate line is broken.

Claim 33: (previously presented) The liquid crystal display as claimed in claim 1, wherein

a distance between the first shielding layer and the first data line is smaller than a distance

between the second shielding layer and the second data line.

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Claim 34: (previously presented) A liquid crystal display, comprising:

a pixel electrode enclosed by a first gate line, a second gate line, a first data line, and a

second data line;

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- a switching element having a drain electrode positioned on the first gate line;
- a first shielding layer having an edge between the first data line and the pixel electrode; and
- a second shielding layer having an edge between the second data line and the pixel electrode;
- wherein the drain electrode traverses a spacing between the first shielding layer and the second shielding layer, and overlaps with the first shielding layer and the second shielding layer.
- 10 Claim 35: (previously presented) A liquid crystal display including a plurality of pixel areas, each pixel area comprising:
 - a pixel area defined by a first gate line, a second gate line, a first data line, and a second data line, the first data line having an extension portion which is disposed perpendicular to the first data line and parallel with the first gate line;
- a pixel electrode formed overlying the pixel area;
 - a switching element electrically connected to the pixel electrode; and
 - a first shielding layer directly connected to the first gate line, the first shielding layer being parallel to the first data line and adjacent to the first data line, and overlapping across the extension portion of the first data line.

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